

DSP series

Transmitter



放大器

位移传感器

Feature:

- Power supply isolation, with DC/DC isolation module.
 - Excellent linearity up to 0.02%.
 - 1ms response time output.
- Excellent temperature drift, typical temperature drift value (25 ppm/K).
- Includes high steady state power chip, low noise, ultra-low noise op amp optional.
 - Zero and full degree can be adjusted.

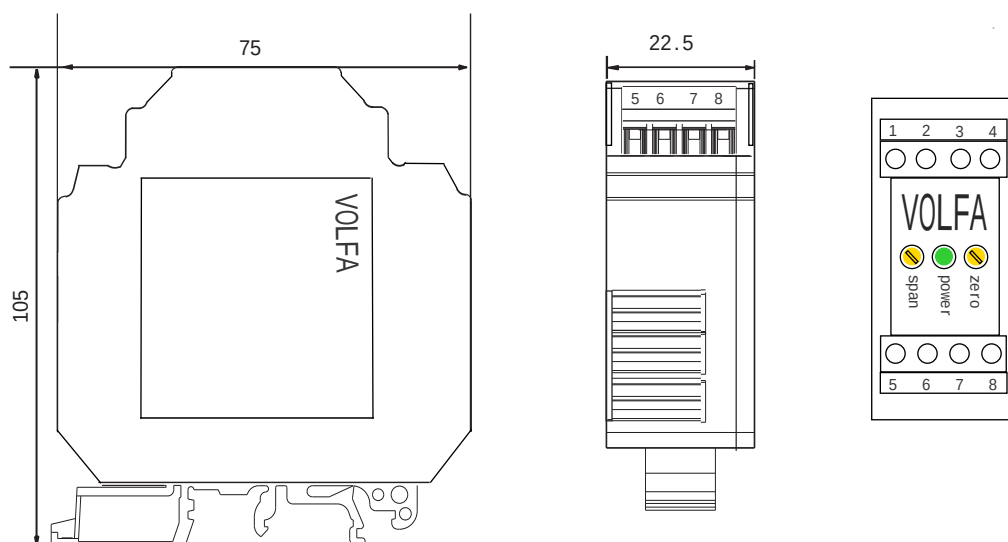
Details:

- Installation method Standard 35mm guide rail fastening
- Working state with power indicator light, reverse protection
- Power isolation The working power supply is isolated from the input signal
- With EMC Level 4 circuit design

Usage:

- Signal conversion devices specially configured for potentiometer type (linear and angular) sensors
- Provide standard analog signal to PLC
- Quality management, industrial data analysis
- Experimental analysis, mechanical manufacturing

Dimensions:



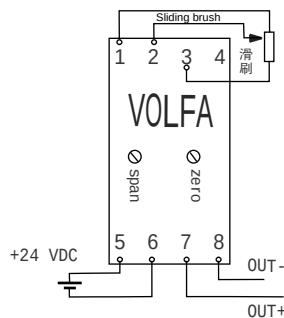
Parameters:

Working voltage	DC 18 ~ 36V Wide range input, power reverse protection
Power consumption	Maximum 24V/25mA
Plastic housing	PA66
Protection class	IP20
Input impedance	0-1KΩ, 0-5KΩ, 0-10KΩ, 0-20KΩ
Output signals	0-5V, 0-10V, 0-20mA, 4-20mA
Operating temperature	-30 + 85°C
Electrical EMC	Level 4
Scope of application	PLC, signal analysis instrument
The refresh rate	less than 1ms
Dimensions	Width 22.5 x height 75 x depth 105 mm
Weight	approx. 100g without packaging
Stable time	15 minutes
Linearity	0.02% (0.05%, 0.1% optional)
Temperature coefficient	25 ppm/K

注：温度系数将影响输出电流和输出电压。若为输出电流信号，则电压要通过连接负载阻抗获取，故负载阻抗的温度系数也必须加以考虑。

Wiring:

Note: The temperature coefficient will affect the output current and output voltage. If it is an output current signal, the voltage is obtained by connecting the load impedance, so the temperature coefficient of the load impedance must also be considered.



端子 1和端子3：接 电位计的全阻端1和3
 端子 2：接 电位计的信号端2
 端子 4：空脚
 端子 5：接 DC的 +24V
 端子 6：接 DC的 0 V
 端子 7：接 输出信号 +
 端子 8：接 输出信号

* (1, 3互换可以改变信号的变化趋势)

Instructions:

DSP - XX - X

系列名
Series

Level 等级

Linearity

A: 线性度 0.02% FS
 B: 线性度 0.05% FS
 C: 线性度 0.1% FS

输出信号种类

optional output signals such as: 如 V1: 0 - 10 V
 V2: 0 - 5 V
 A1: 4 - 20 mA
 A2: 0 - 20 mA

The module terminals 1 and 3 are connected to the full stop terminals 1 and 3 of the potentiometer
 The module terminal 2 is connected to the signal end 2 of the potentiometer
 Module terminal 4 is disconnected
 Module terminal 5 connects to DC +24V
 Module terminal 6 connects to DC 0V
 Module terminal 7 connects to output signal +
 Module terminal 8 connects to the output signal
 PS: Interchanging terminals 1 and 3 can change the signal trend